

101

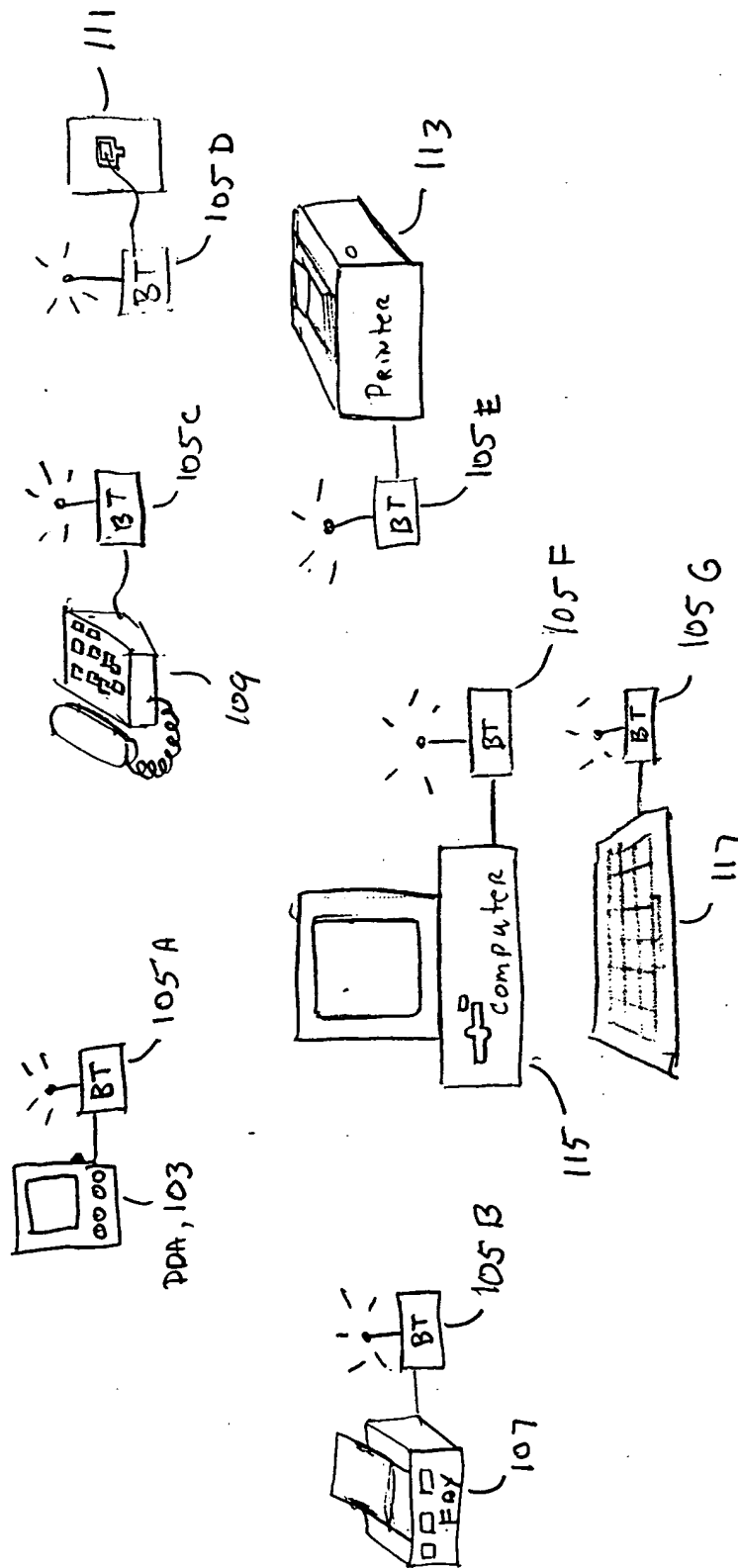


Figure #1

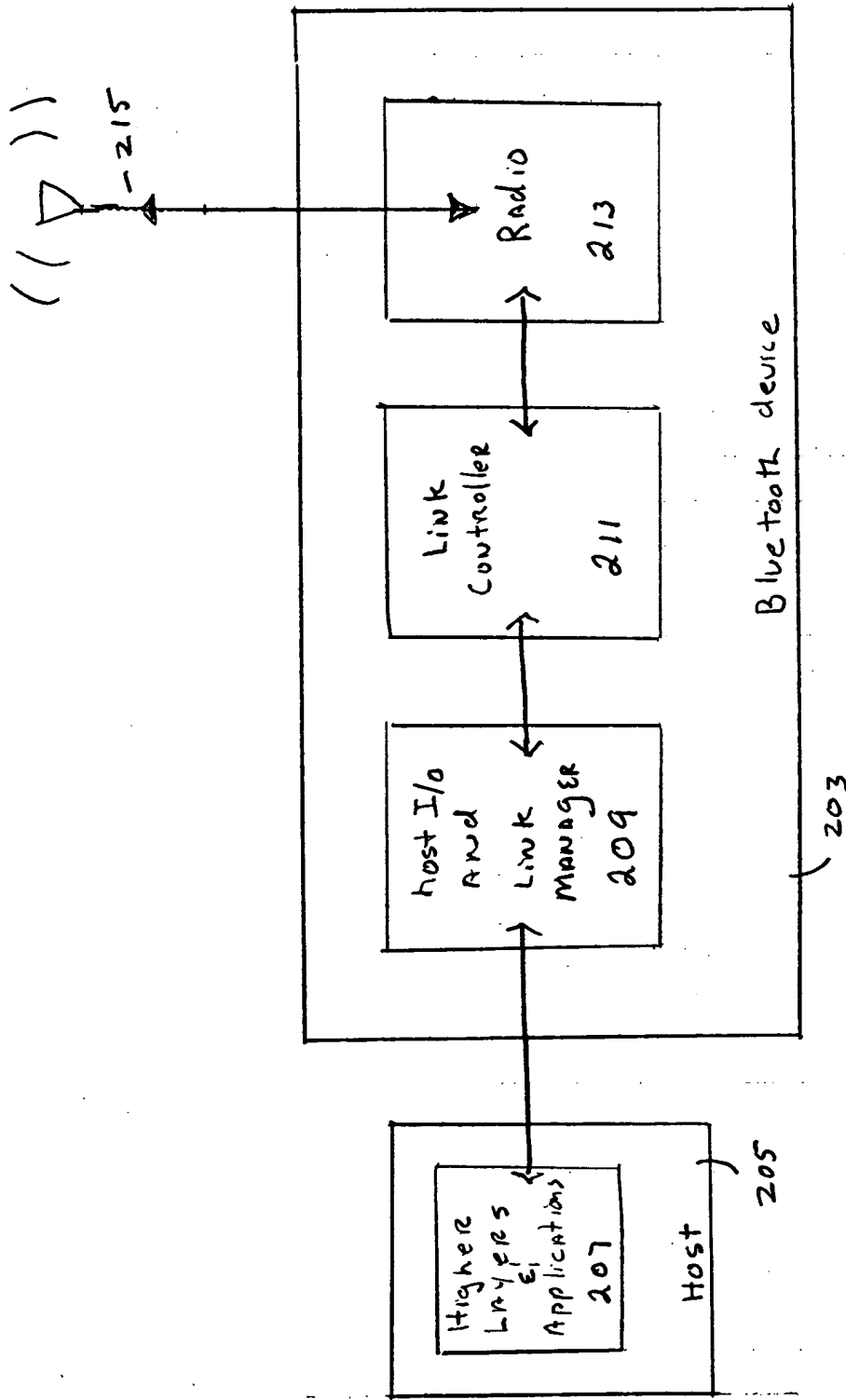


Figure 2A

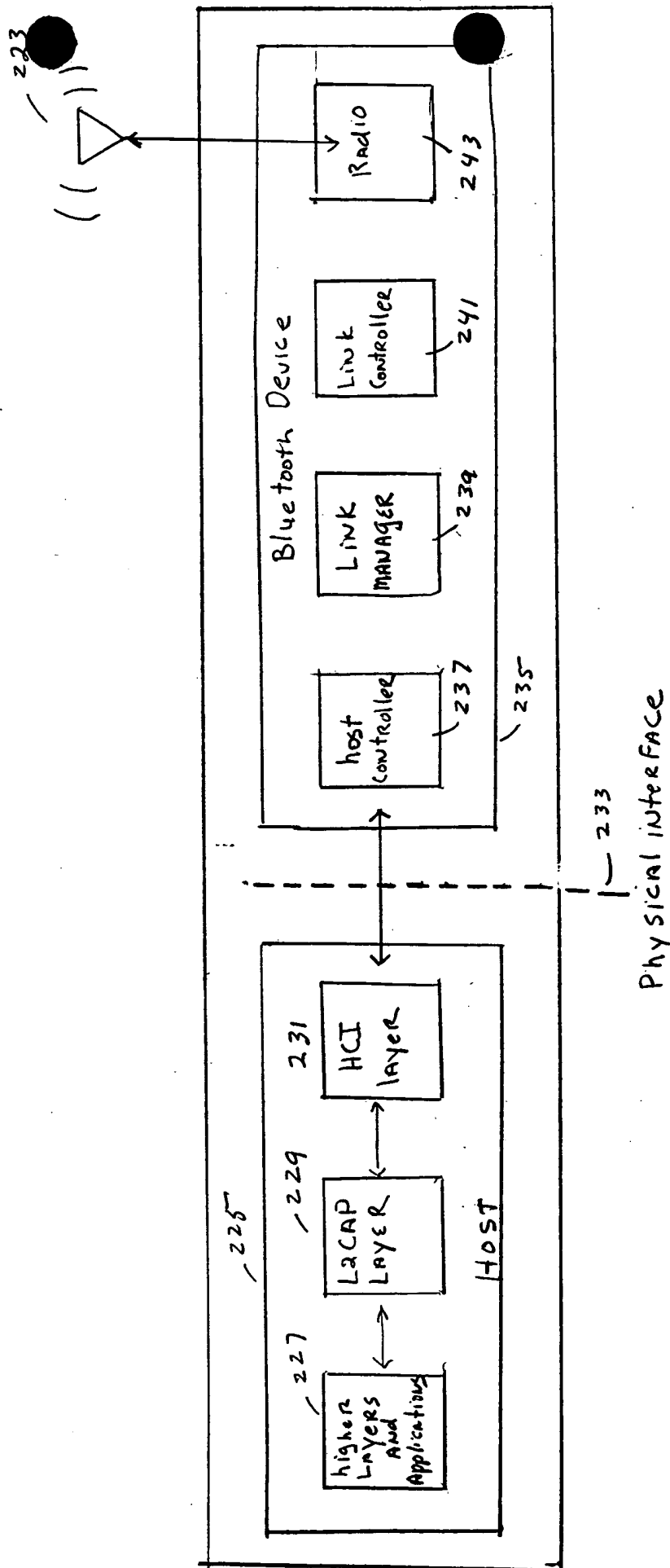


Figure 2B

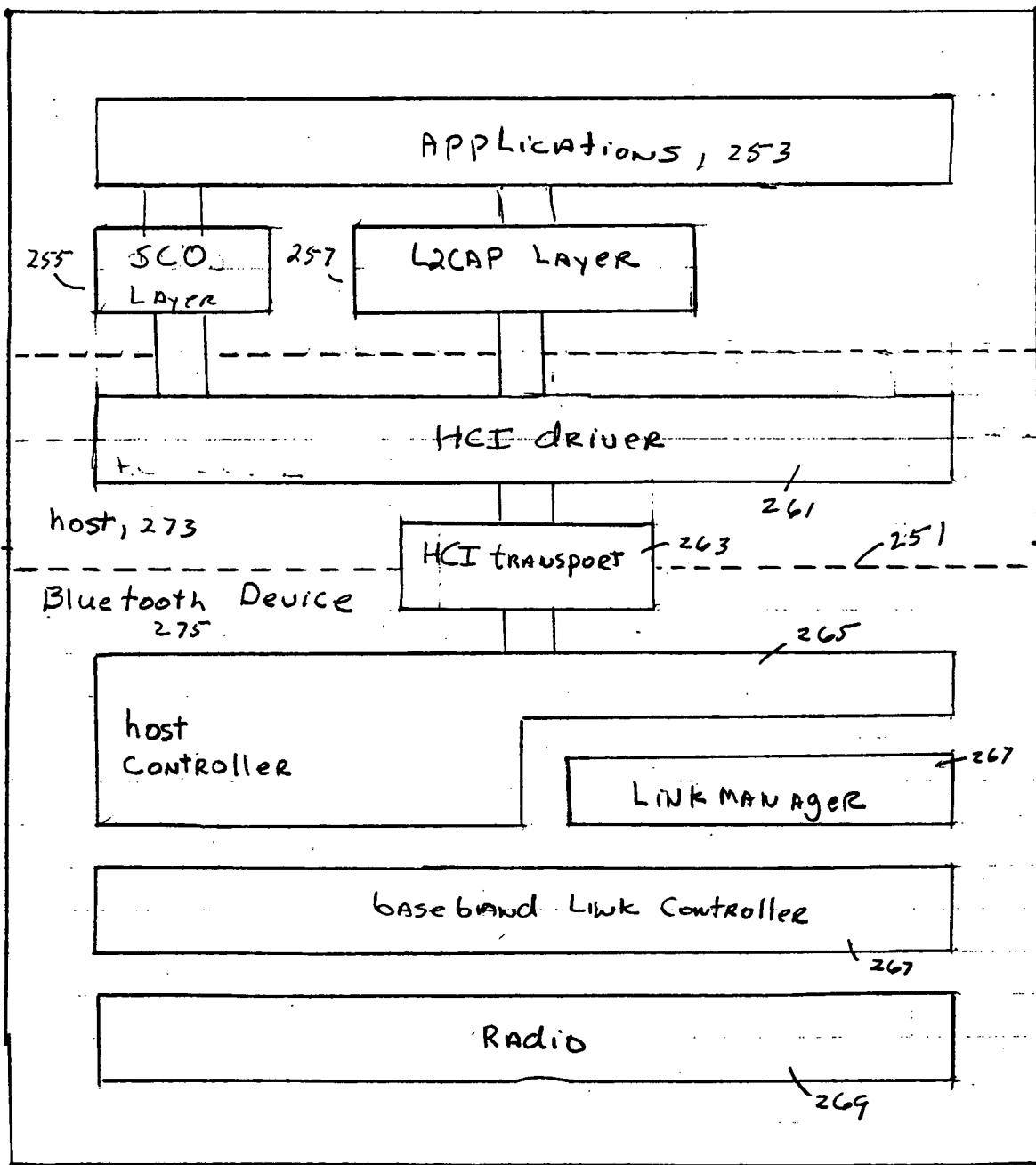


Figure 2C

301

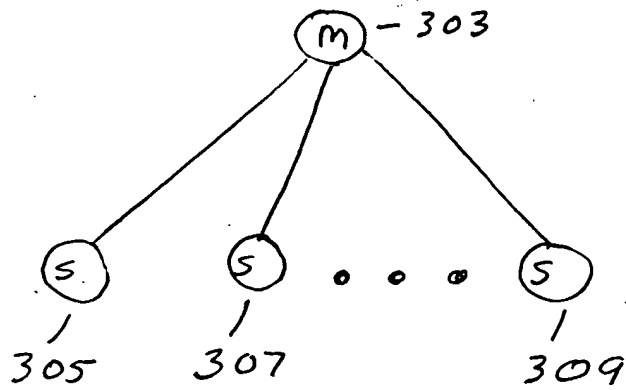


Figure #3

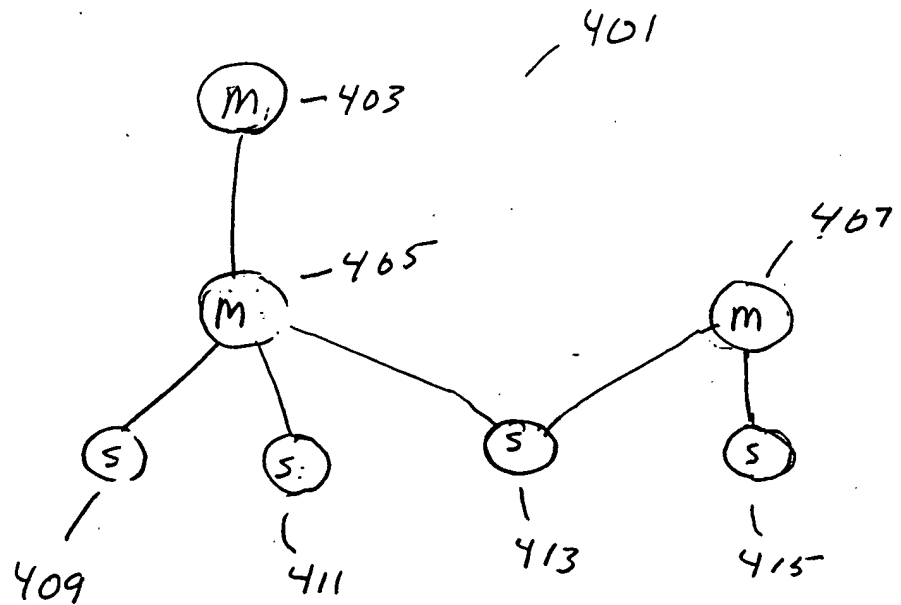


Figure #4

00288054 024504

TESTED: 19080200

50+

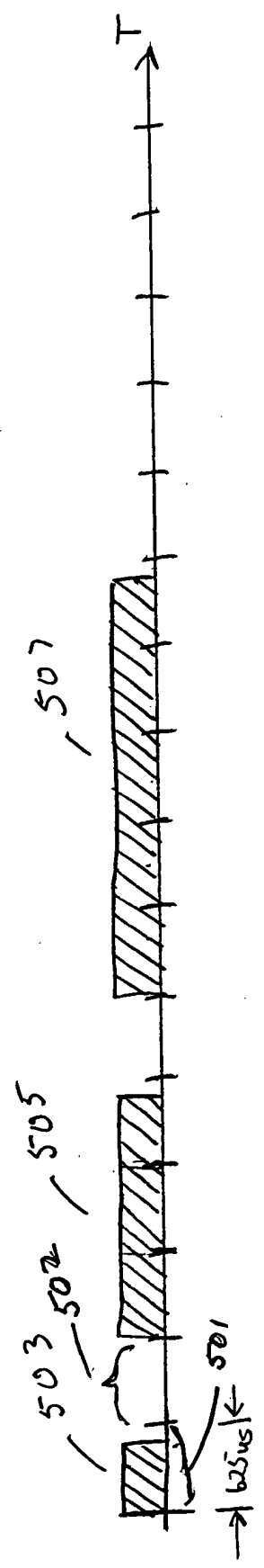
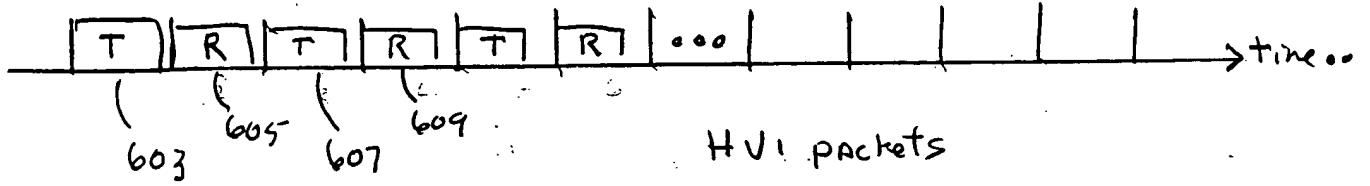
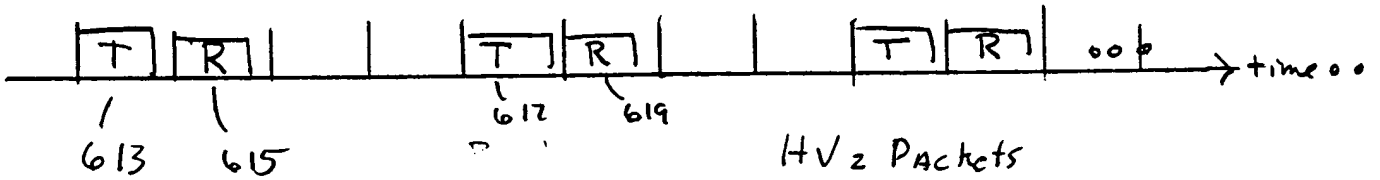


Figure 5

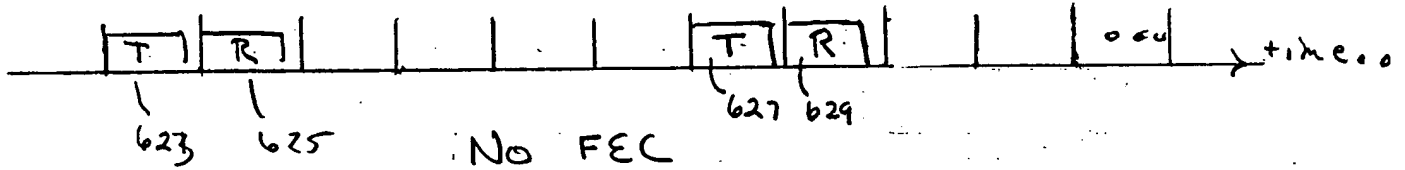
601



611



621



HV3 packets

Fig 6

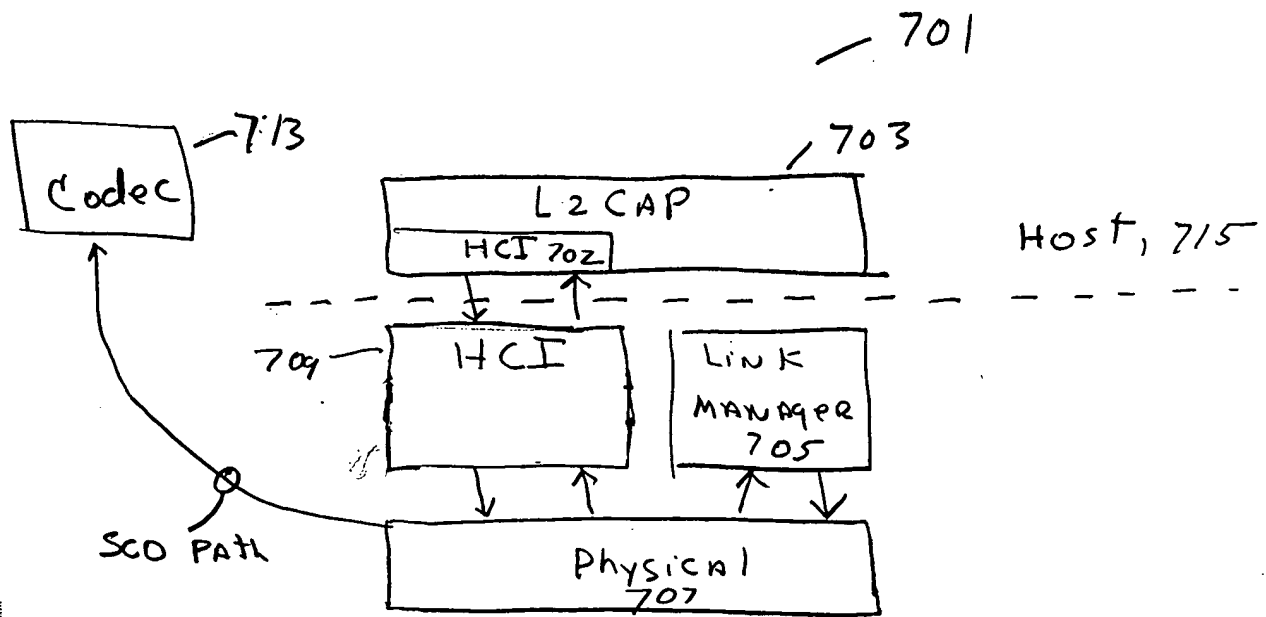


Figure #7

[illegible]

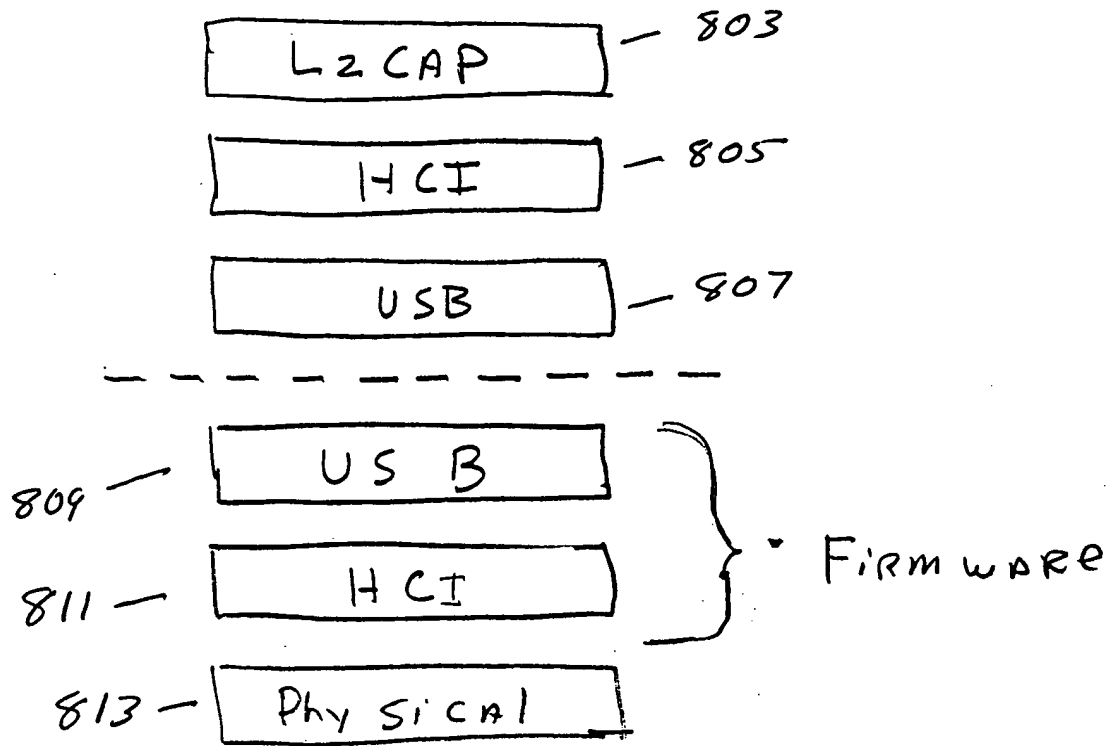


Figure 8

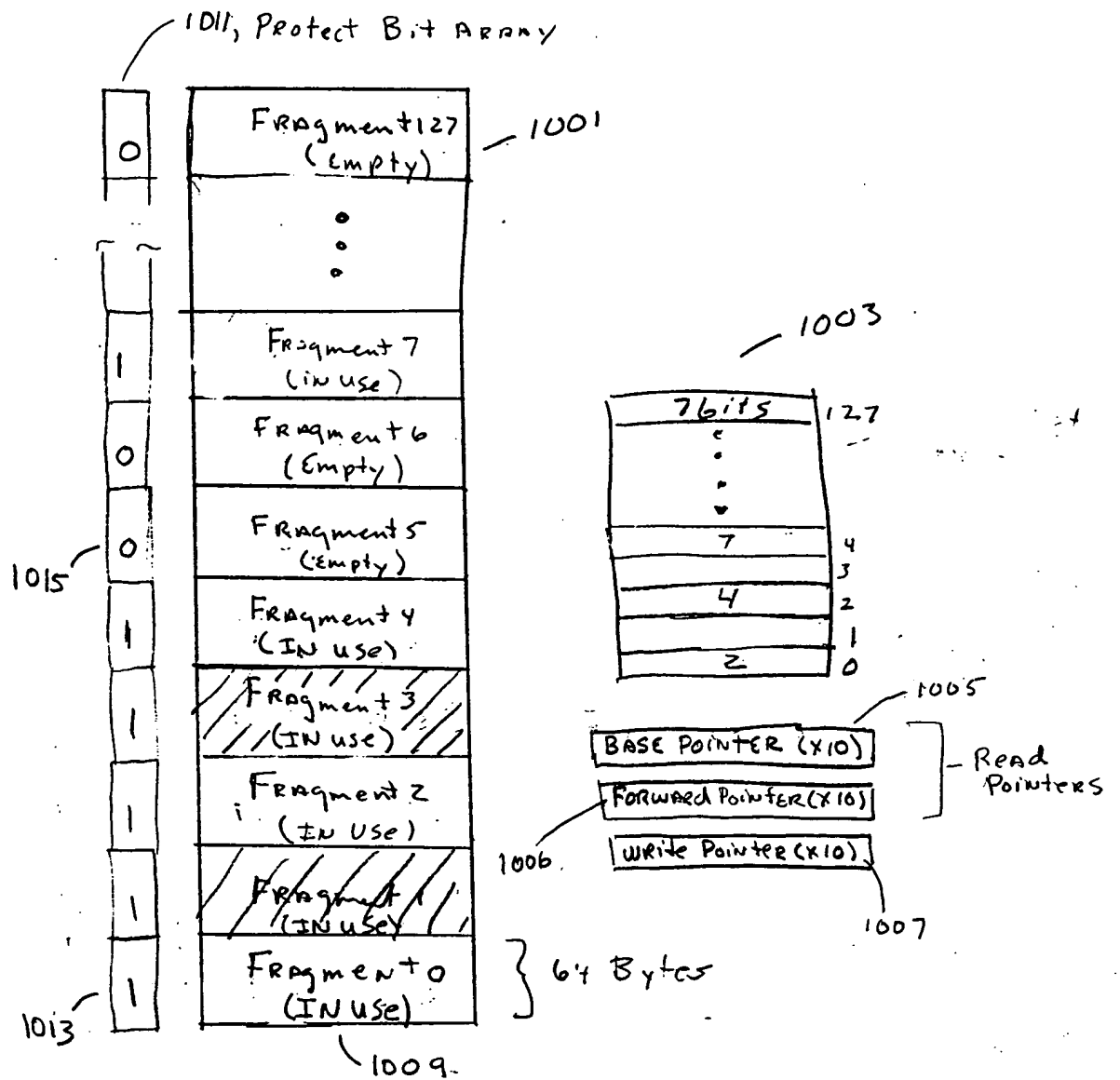


Figure # 10

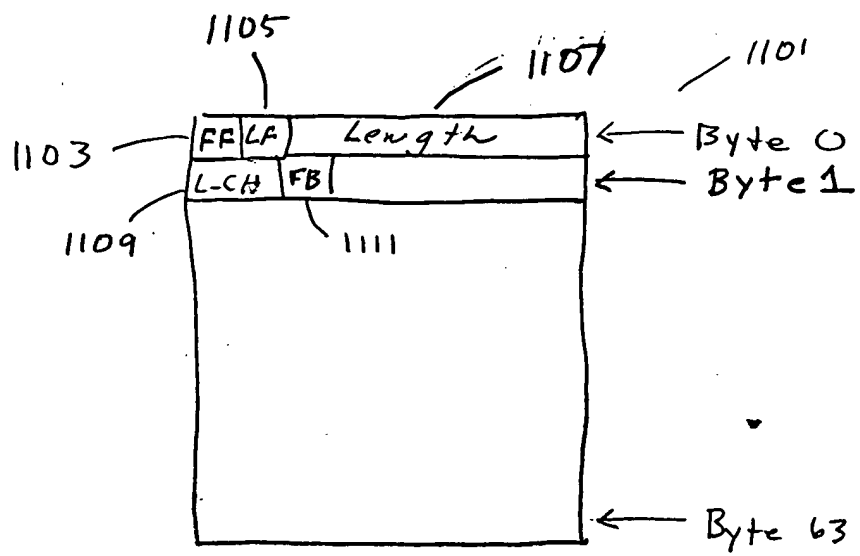


Figure # 11

0928054-034504

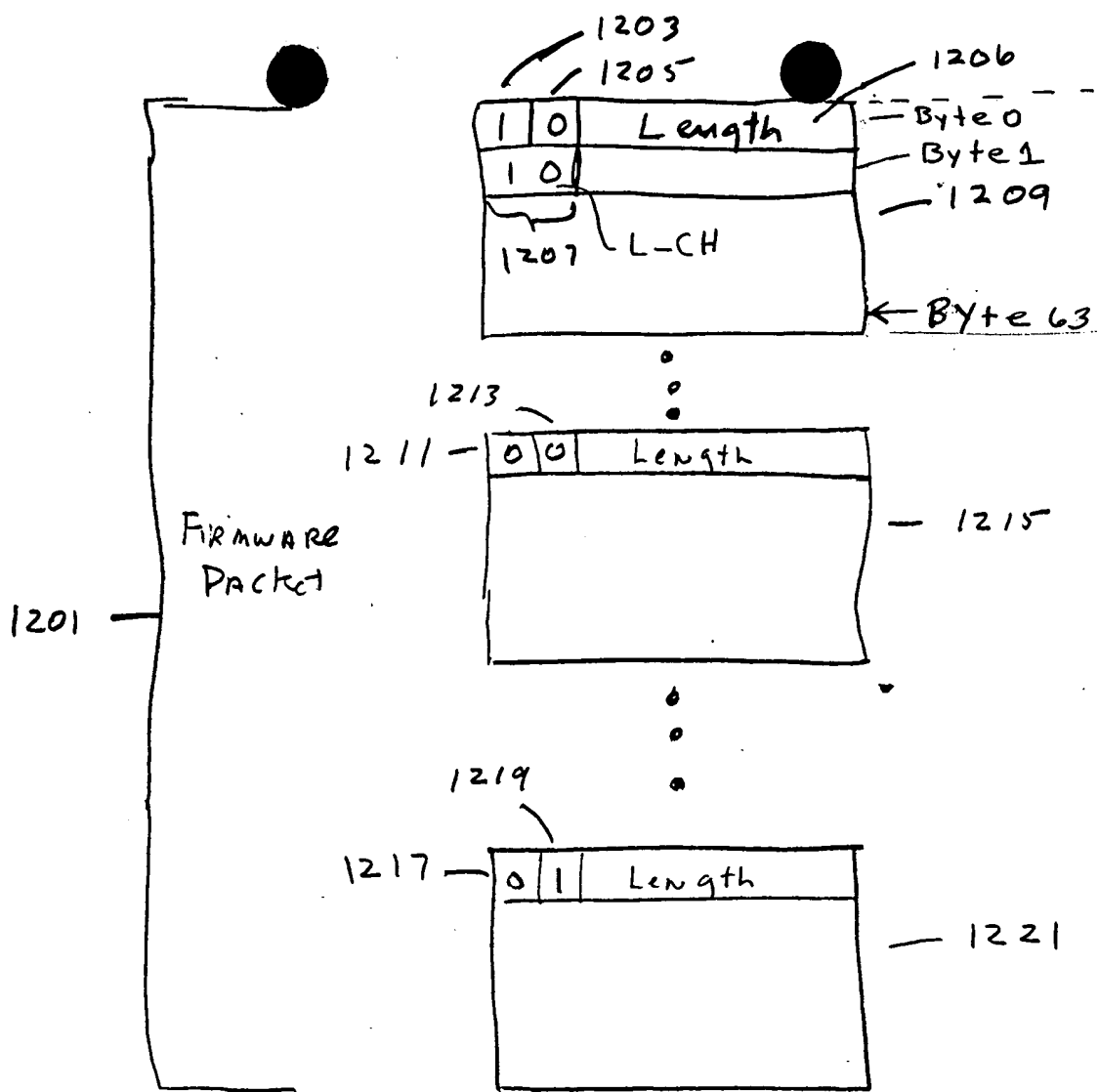


Figure 12

Figure 13

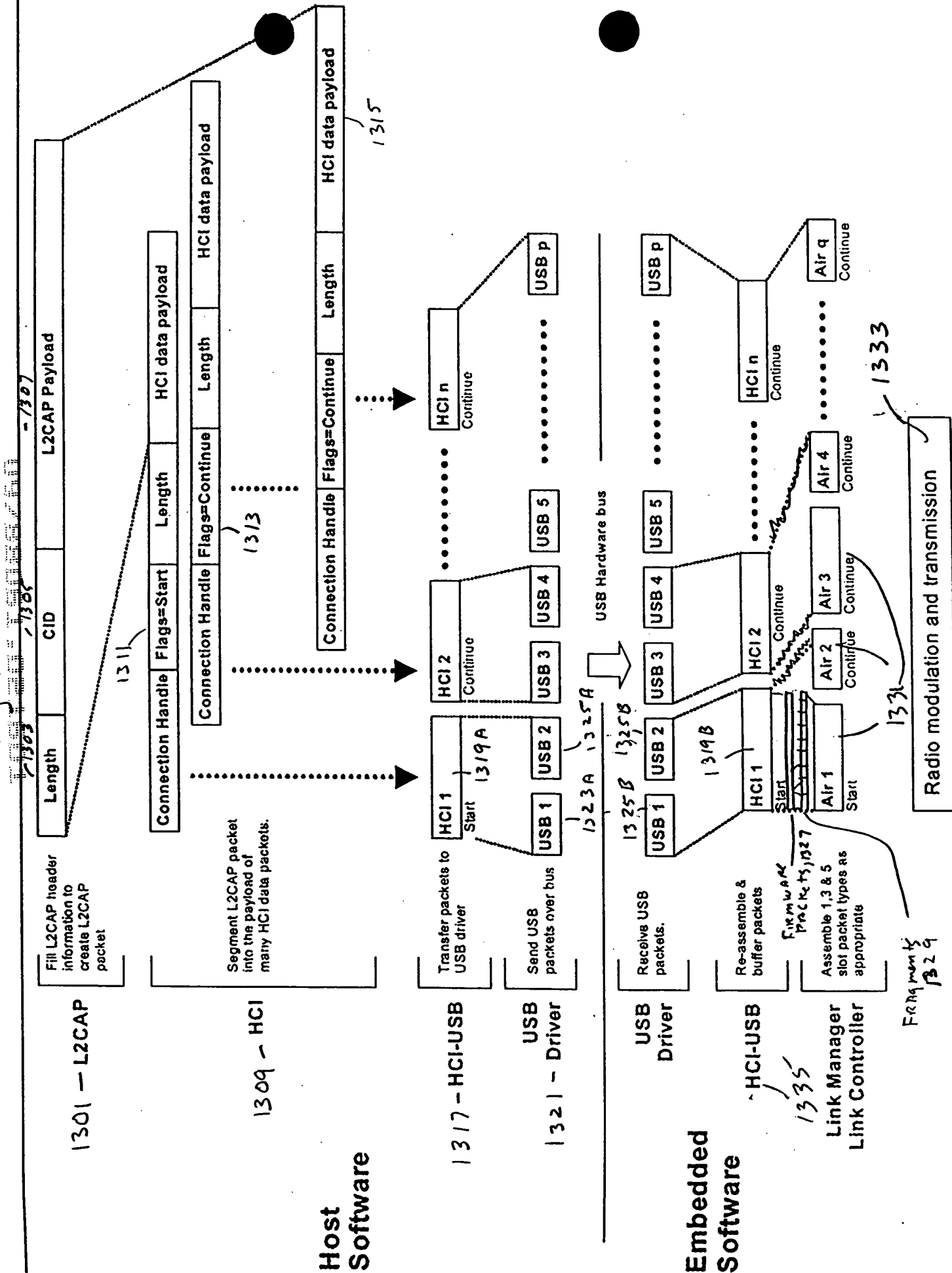
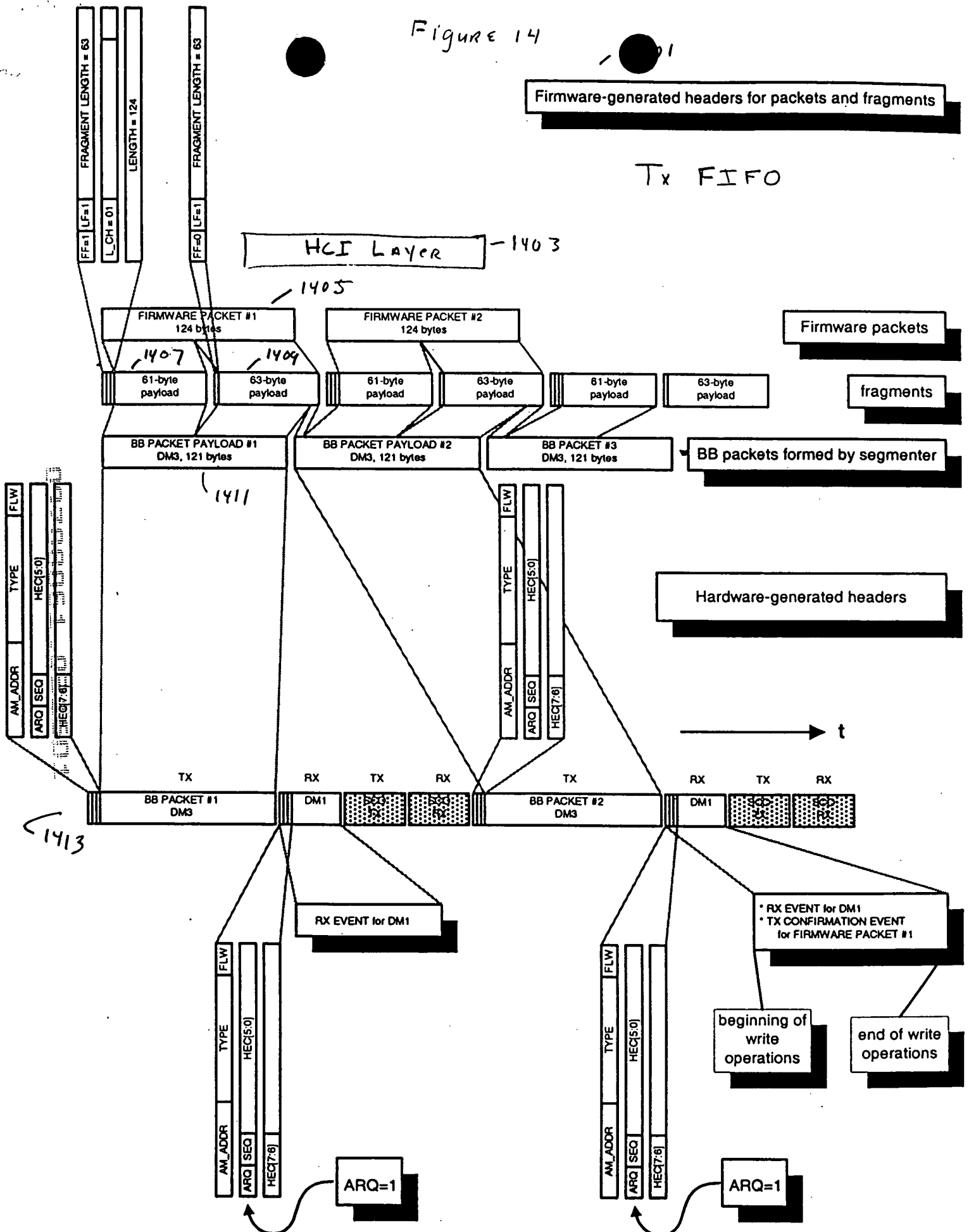


Figure 14

Firmware-generated headers for packets and fragments

Tx FIFO



00790964 004594

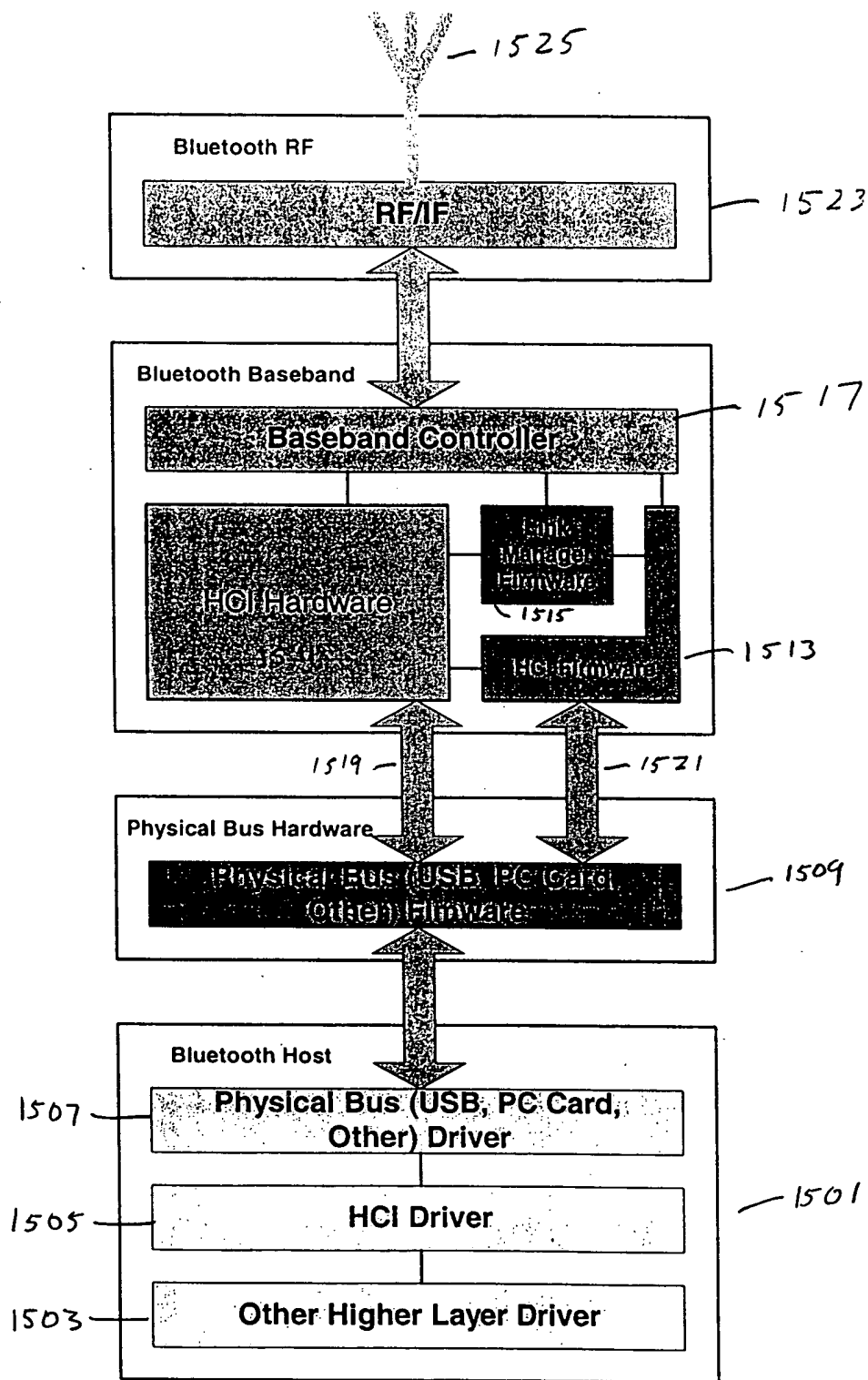


Figure 15

1601

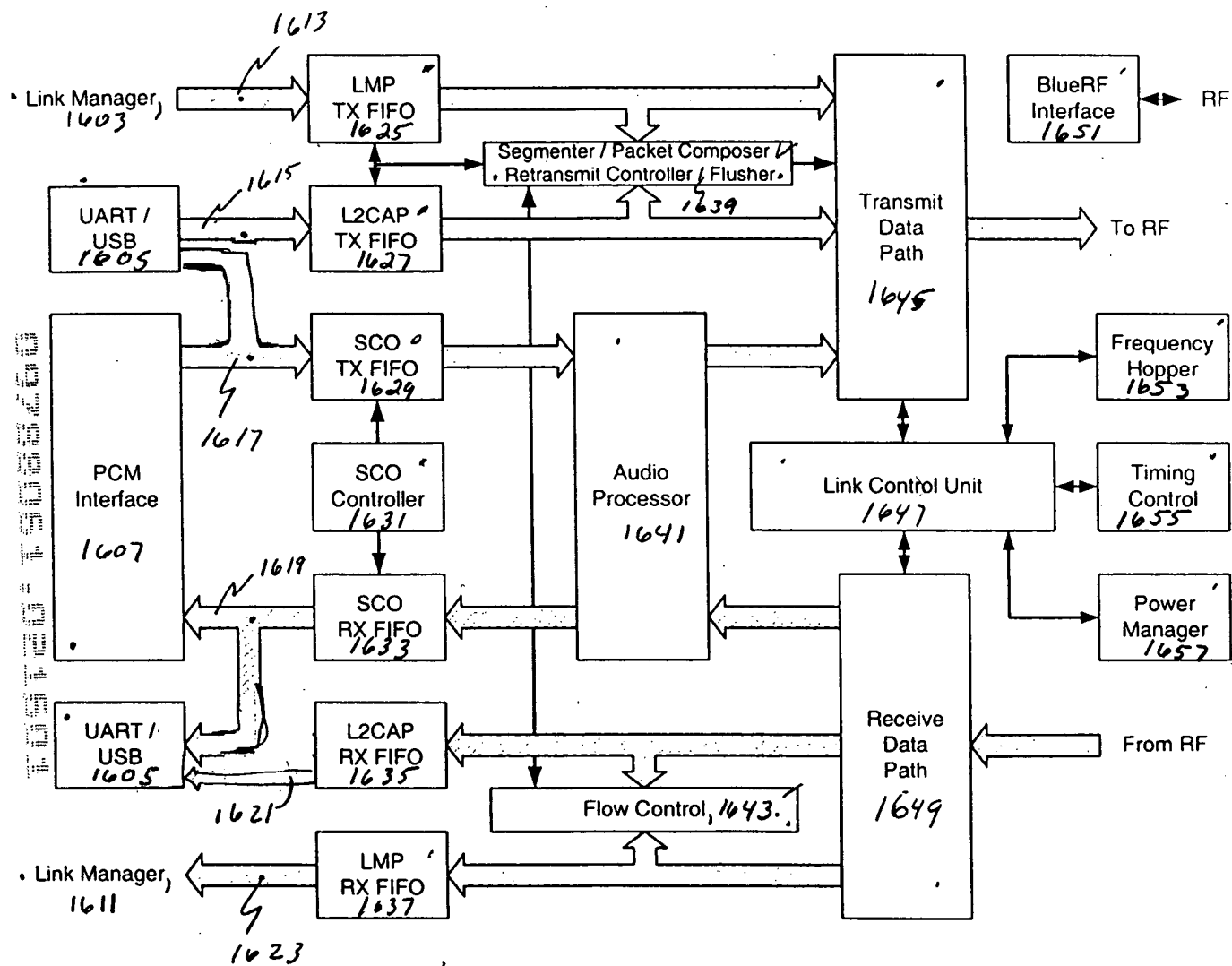


FIGURE 16

1001 1001 1001 1001

1701

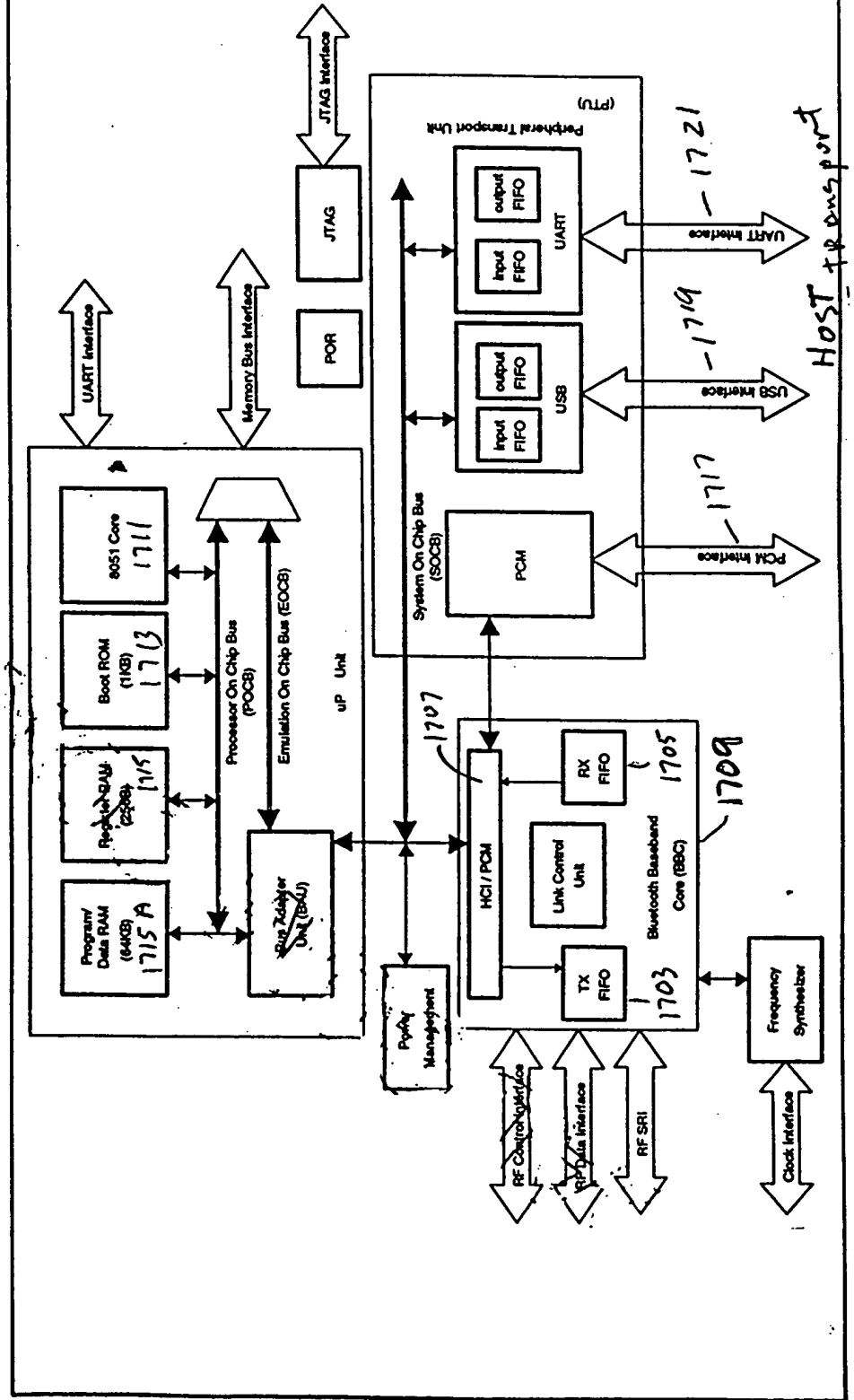


Figure 17

1801

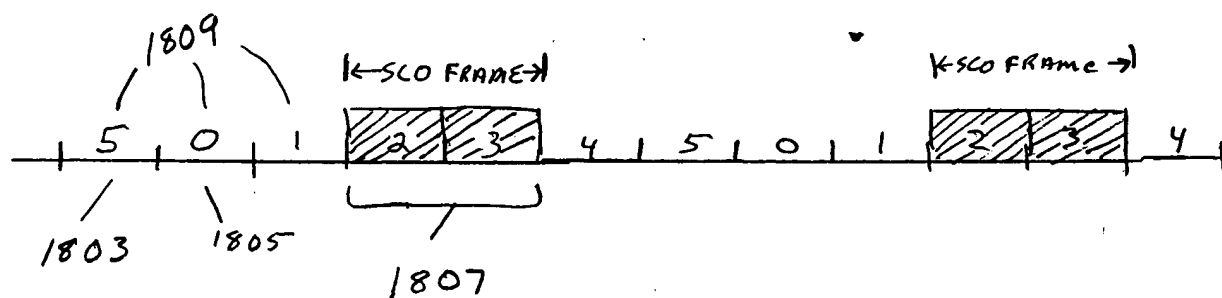


Figure 18

1901

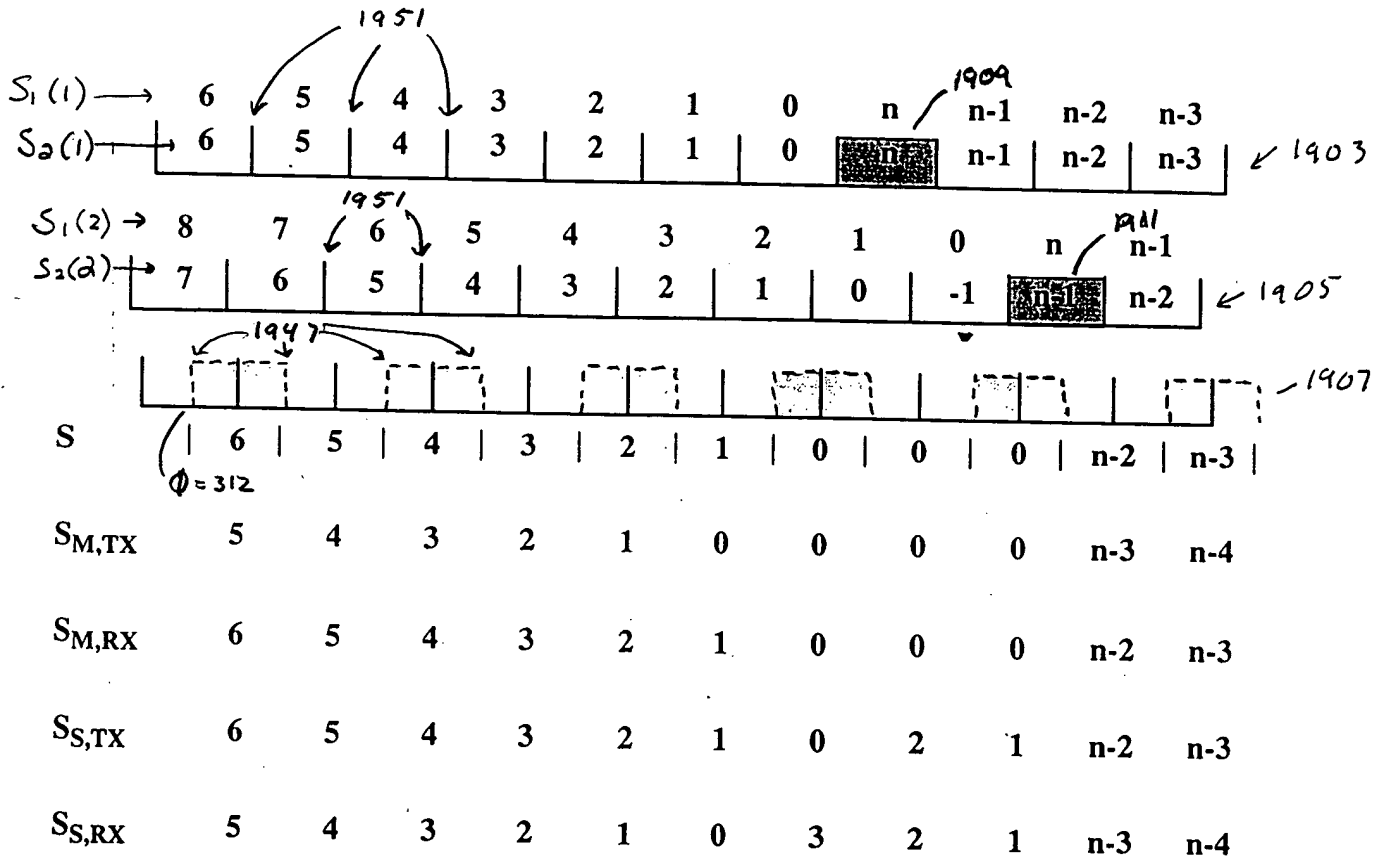


Figure 19A

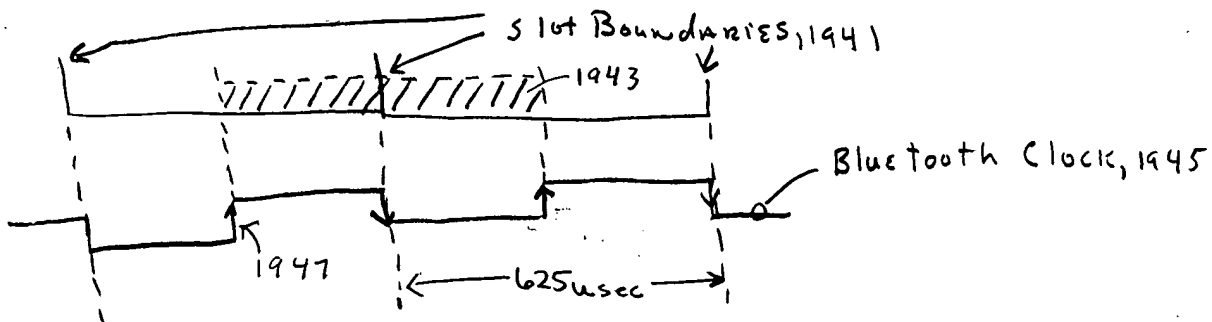


Figure 19B

Table 1. Packet Type Priority

2001

Range Label	Min Bytes In Buffer	Max Bytes In Buffer	1 st Choice	2 nd Choice	3 rd Choice	4 th Choice	5 th Choice	6 th Choice
a	0	0	NULL	NULL	NULL	NULL	NULL	NULL
b	1	17	DM1	DM1	DM3	DM3	DM5	DM5
c	18	27	DM1	DM3	DM3	DM5	DM5	DM1
d	28	121	DM3	DM3	DM5	DM5	DM1	DM1
e	122	133	DM3	DM5	DM5	DM3	DM1	DM1
f	134	224	DM5	DM5	DM3	DM3	DM1	DM1
g	225	339	DM5	DM5	DM3	DM3	DM1	DM1
h	339		DM5	DM5	DM3	DM3	DM1	DM1

Figure 20

105120-19086200

Figure 1. Example of a Fragment Chooser for 16 fragments, $N=4$

0 = UN-PROTECTED
1 = PROTECTED

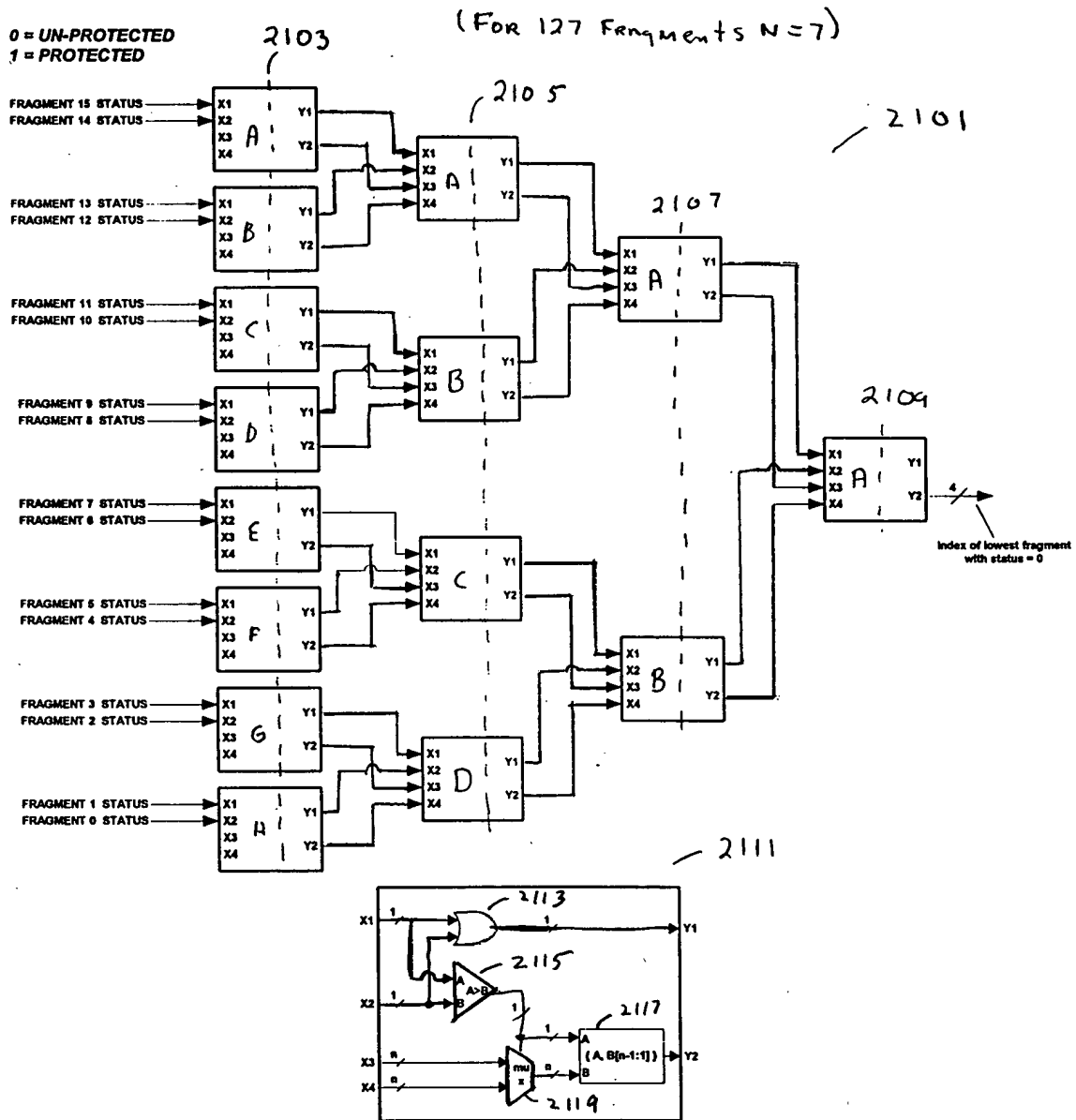


Figure 21

Figure 22 Circuit to calculate CLK mod T, where CLK is 27 bits and T is 8 bits.

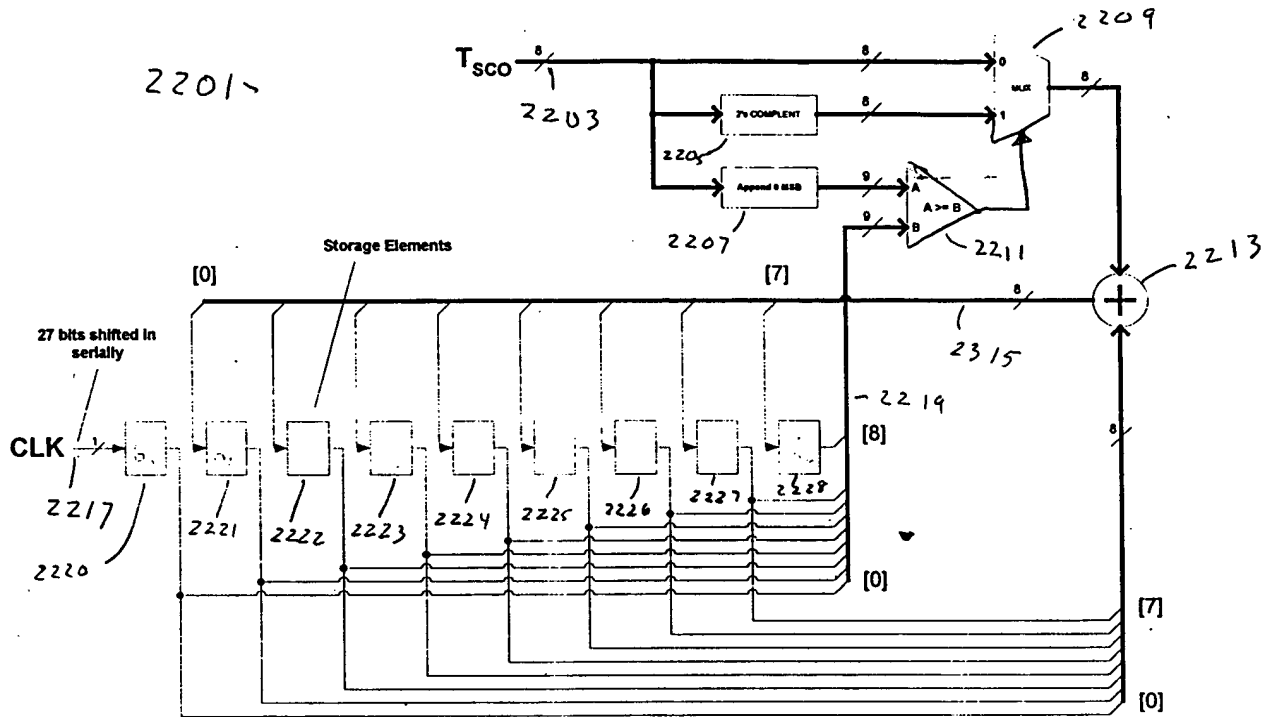


Figure 22

Figure 3. Example calculation: $115307261 \bmod 135$

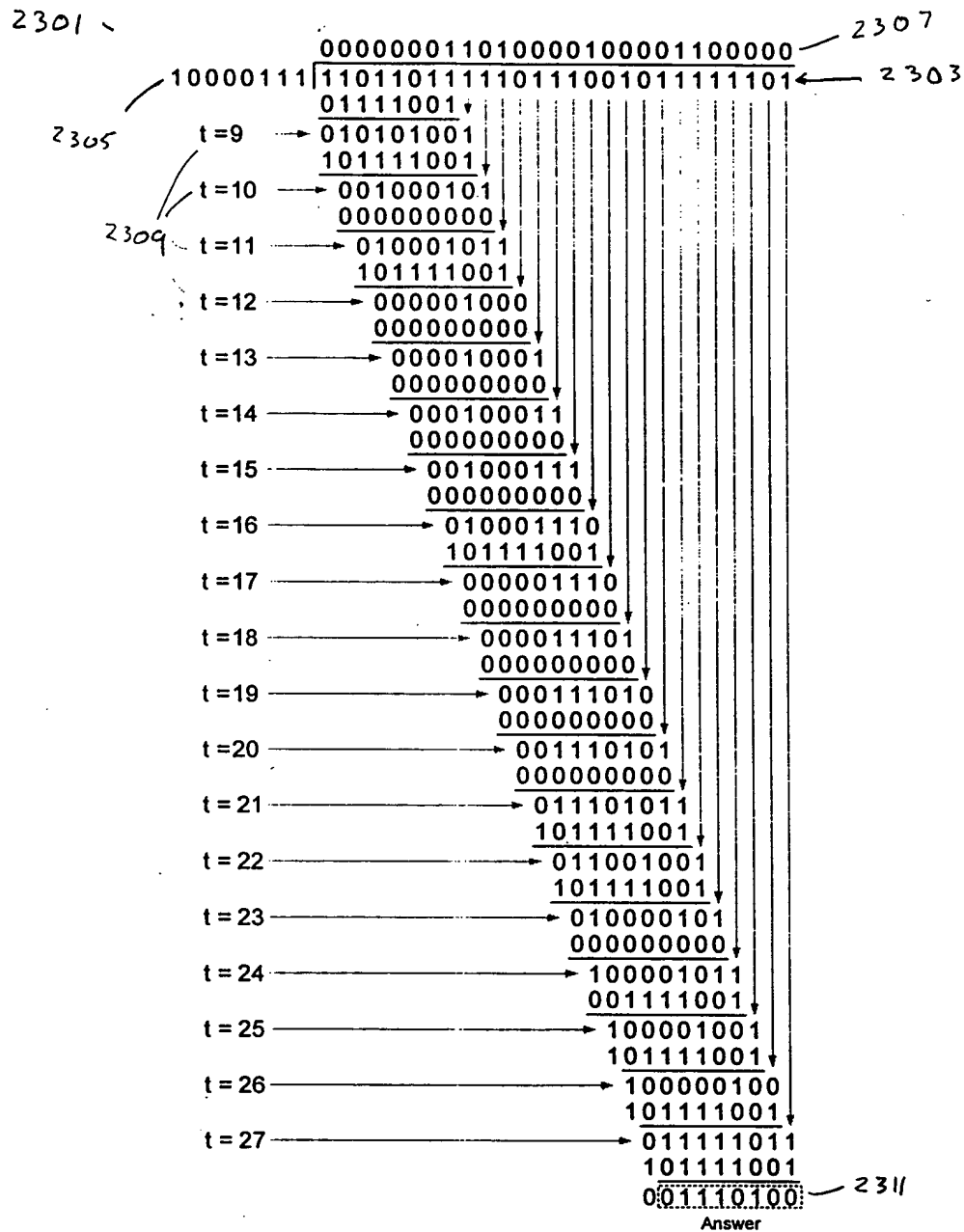


Figure 23

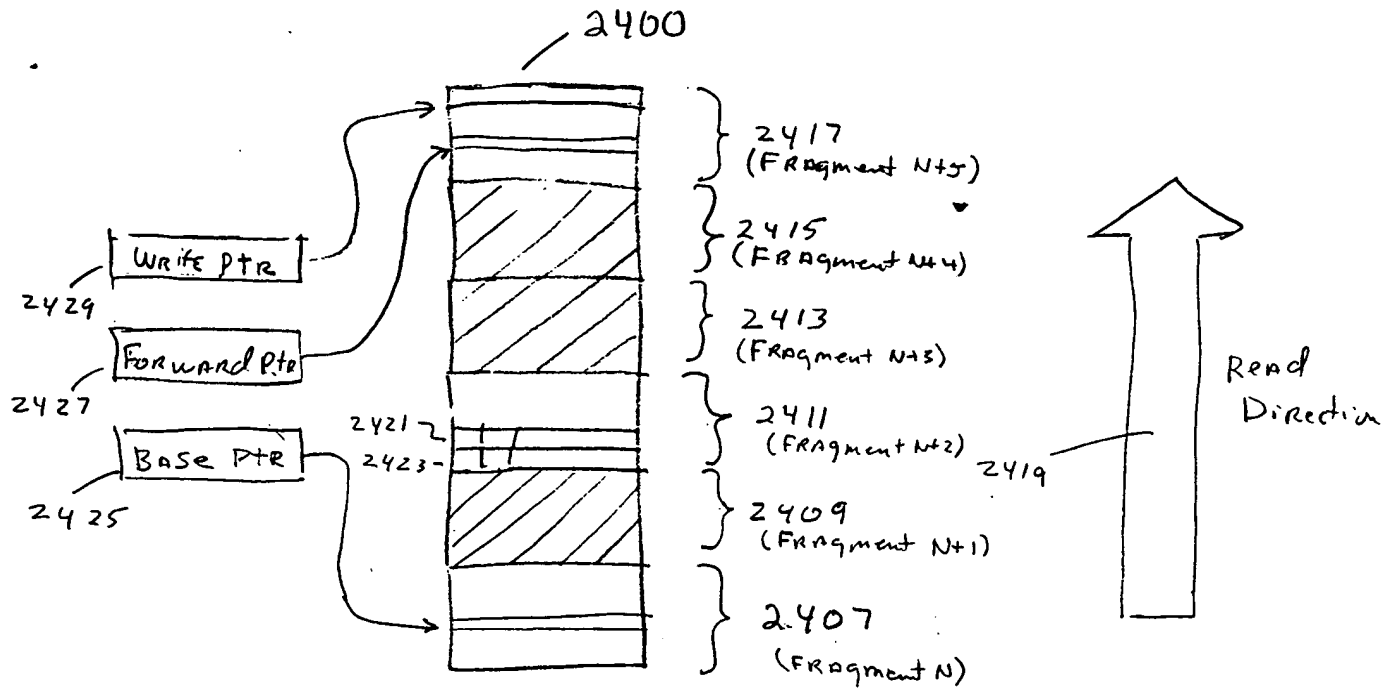
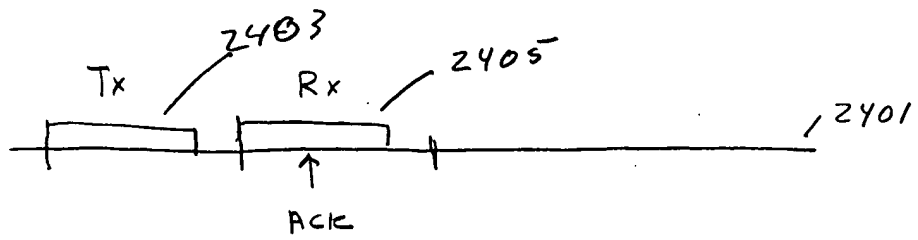


Figure 24

00000-15000000

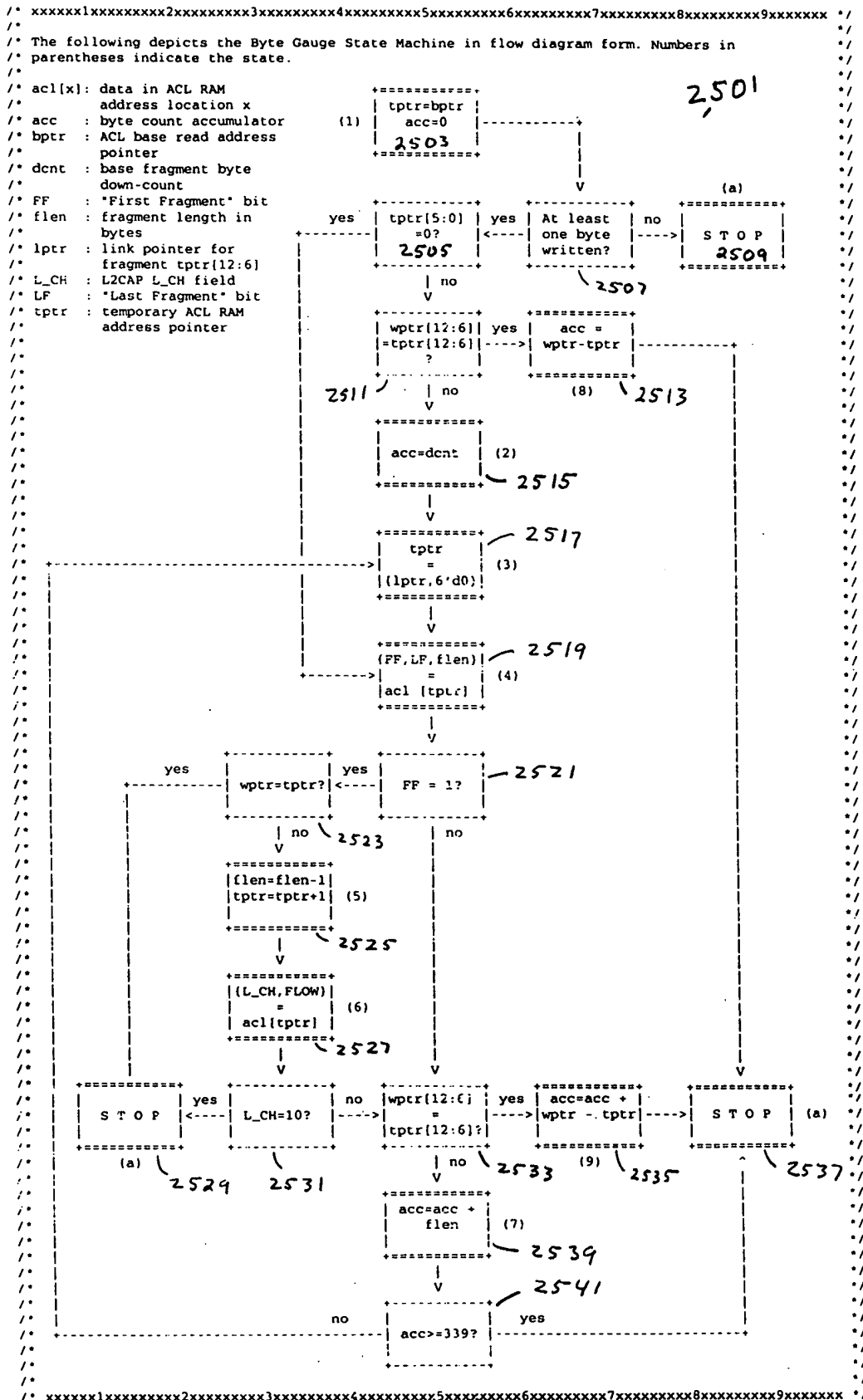


Figure 25

L2CAP PACKET FLUSH STATE MACHINE

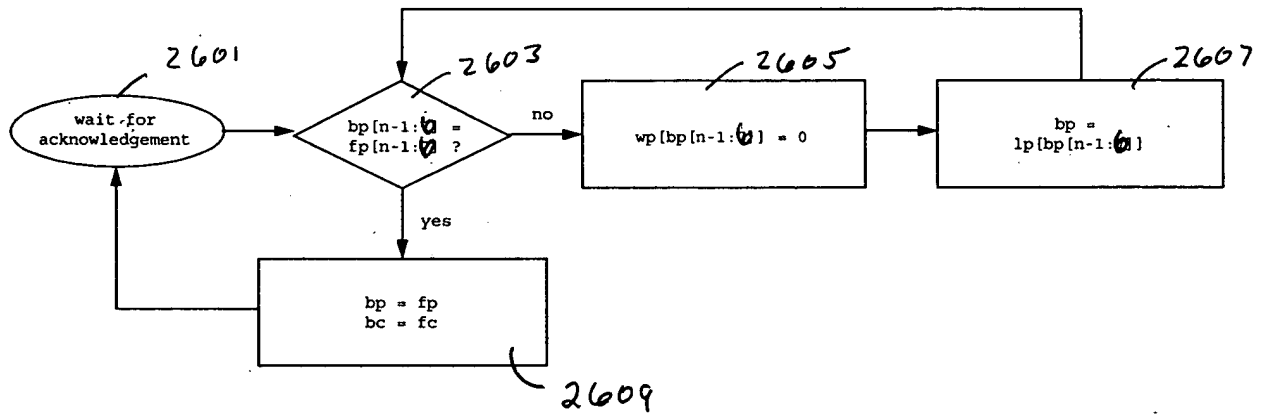


Figure 26

L2CAP PACKET TRANSMIT STATE MACHINE

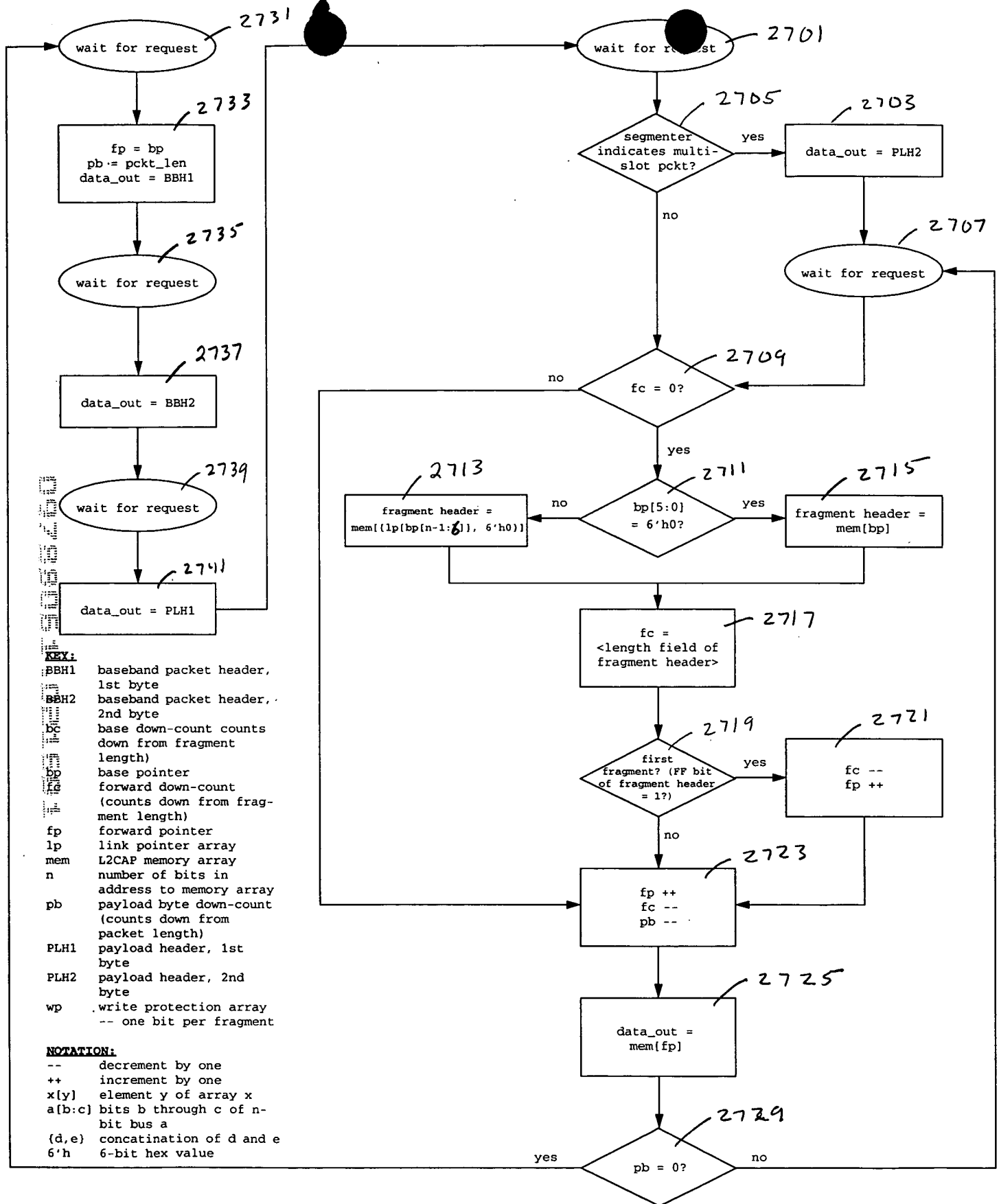


Figure 27